

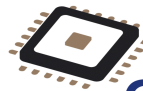
nSilitation has extensive and proven knowledge in the area of radiation resilient IC design as well as measurement and characterization of radiation effects on ICs.

We have know-how and techniques applicable to integrated circuit design in ordinary, non-hardened processes to increase circuit resilience against radiation effects - both for total ionization dose (TID) and for single event effects (SEE).

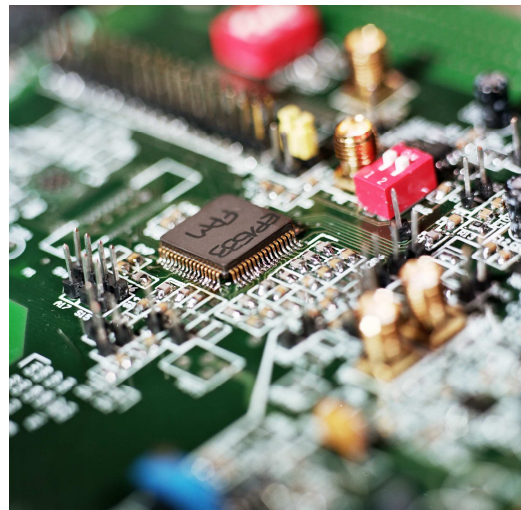
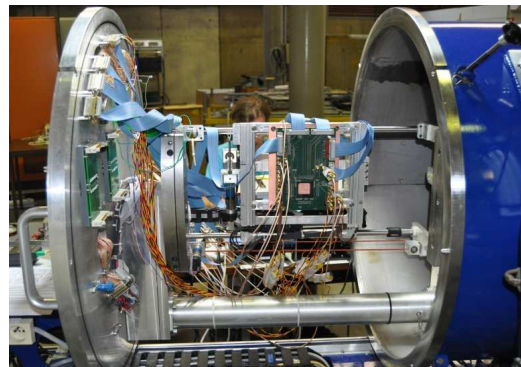
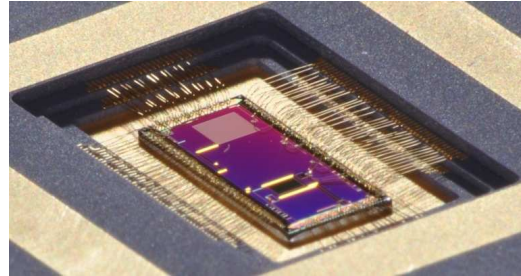
nSilitation also measures and characterizes the effects of radiation on any electronic device. For instance, it is of highest importance for ITER that the effects of intense gamma ray radiation are tested on the electronic systems which will be used in the vessel during inspection.

nSilitation specializes in the design and qualification of Application Specific ICs (ASICs) or Systems on a Chip (SoCs) combining advanced analog features with industry standard μ Controllers, ROM, RAM, non-volatile memory and custom designed digital cells. nSilitation also specifies and designs any advanced electronic system, from architecture study to production.

nSilitation believes that ITER is a place where Radiation Hardened electronics are needed, as in the case for Space and Avionics applications. Hence nSilitation's engineers would like to provide their know-how and experience to serve the ITER project community.



nSilitation



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